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The paper presents a real-time transceiver using an Orthogonal Frequency-Division Multiplexing (OFDM) signaling scheme. The transceiver is implemented on a FieldProgrammable Gate Array (FPGA) through Xilinx System Generator for DSP and includes all the blocks needed for the transmission path of OFDM.

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Results show that a complete Alamouti scheme can be efficiently implemented using an FPGA, and a real-time implementation of 2×1 MIMO system using the Alamouti scheme is presented. In this paper, we present a real-time implementation of 2×1 MIMO system using the Alamouti scheme. The real-time chain is based on the ...

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by A Almajdoob · 2012 · Cited by 3 — Design and implementation of QPSK-OFDM system in hardware based on downlink physical layer parameters for 3GPP LTE standard with throughput of 20 Mbps. « Proposing a new design flow methodology of different stages from modeling and simulation to hardware implementation for system design and FPGA implementations.