

Modern Digital Electronics By R P Jain

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Modern Digital Electronics By R P Jain

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(Chapter-1 Introduction): Boolean Algebra, Types of Computer, Functional units of digital system and their interconnections, buses, bus architecture, types of buses and bus arbitration. Register, bus and memory transfer. Processor organization, general registers organization, stack organization and addressing modes.

(Chapter-2 Arithmetic and logic unit): Look ahead carries adders. Multiplication: Signed operand multiplication, Booth's algorithm and array multiplier. Division and logic operations. Floating point arithmetic operation, Arithmetic & logic unit design. IEEE Standard for Floating Point Numbers

(Chapter-3 Control Unit): Instruction types, formats, instruction cycles and sub cycles (fetch and execute etc), micro-operations, execution of a complete instruction. Program Control, Reduced Instruction Set Computer, Hardwire and micro programmed control: micro programme sequencing, concept of horizontal and vertical microprogramming.

(Chapter-4 Memory): Basic concept and hierarchy, semiconductor RAM memories, 2D & 2 1/2D memory organization. ROM memories. Cache memories: concept and design issues & performance, address mapping and replacement Auxiliary memories: magnetic disk, magnetic tape and optical disks Virtual memory: concept implementation.

(Chapter-5 Input / Output): Peripheral devices, I/O interface, I/O ports, Interrupts: interrupt hardware, types of interrupts and exceptions. Modes of Data Transfer: Programmed I/O, interrupt initiated I/O and Direct Memory Access., I/O channels and processors. Serial Communication: Synchronous & asynchronous communication, standard communication interfaces.

(Chapter-6 Pipelining): Uniprocessing, Multiprocessing, Pipelining

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(Chapter-1 Boolean Algebra & Logic Gates): Introduction to Digital Electronics, Advantage of Digital System, Boolean Algebra, Laws, Not, OR, AND, NOR, NAND, EX-OR, EX-NOR, AND-OR, OR-AND, Universal Gate Functionally Complete Function.

(Chapter-2 Boolean Expressions): Boolean Expressions, SOP(Sum of Product), SOP Canonical Form, POS(Product of Sum), POS Canonical Form, No of Functions Possible, Complementation, Duality, Simplification of Boolean Expression, K-map, Quine Mc-Clusky Method.

(Chapter-3 Combinational Circuits): Basics, Design Procedure, Half Adder, Half subtractor, Full Adder, Full Subtractor, Four-bit parallel binary adder / Ripple adder, Look ahead carry adder, Four-bit ripple adder/subtractor, Multiplexer, Demultiplexer, Decoder, Encoder, Priority Encoder

(Chapter-4 Sequential Circuits): Basics, NOR Latch, NAND Latch, SR flip flop, JK flip flop, T(Toggle)

flip flop, D flip flop, Flip Flops Conversion, Basics of counters, Finding Counting Sequence Synchronous Counters, Designing Synchronous Counters, Asynchronous/Ripple Counter, Registers, Serial In-Serial Out (SISO), Serial-In Parallel-Out shift Register (SIPO), Parallel-In Serial-Out Shift Register (PISO), Parallel-In Parallel-Out Shift Register (PIPO), Ring Counter, Johnson Counter
(Chapter-5 (Number Sysem& Representations): Basics, Conversion, Signed number Representation, Signed Magnitude, 1's Complement, 2's Complement, Gray Code, Binary-Coded Decimal Code (BCD), Excess-3 Code.

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