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A. drive in diffusion of dopants and carriers B. band to band transition dominants over impurity ionization C. impurity ionization dominants over band to band transition D. band to band transition is balanced by impurity ionization

low copper loss low eddy current loss low resistivity higher specific gravity compared to iron PIN diode Tunnel diode Schottky diode

collector current base current emitter current base current or emitter current

tunnel diode MOSFET JFET photo diode

emitter current and emitter to base voltage emitter current and collector to emitter voltage MOSFET PIN diode Tunnel diode UJT

Zener diode PIN diode Tunnel diode Photo diode

Tunnel diode Photo diode PIN diode Schottky diode

NPN transistor Tunnel diode JFET MOSFET

Silver Aluminium Tungsten Platinum

PIN diode Zener diode Schottky diode Photo diode

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core saturation working mask barrier potential low resistivity Frequency

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WELCOME TO LOTUS

(b) Vce=Vcc . (c) Vce has negative value (d) Ic is maximum

The h-parameters of a transistor depends on its (a) configuration (b) operating point (c) temperature. The smallest of the four h-parameters of a transistor is (a) hi (b) hr (c) ho (d) hf

A transistor is operated as a non-saturated switch to eliminate (a) storage time (b) turn-off time (c) turn on time (d) delay time

The effective ß of a Darlington pair using transistors of ß values 50 and 100 is (a) 5000

If the value of a is 0-9 then value of B is (a) 9

If a=0.98, then ratio Iceo/Ico is (a) 50 (b) 0.04

When a transistor is fully switched On, it is said to be (a) shorted (b) saturated (c) open (d) cut-off In the case of a BJT, a is (a) positive and 1 (b) positive and 1

If the common base DC current gain of a BJT is 0.98, it's common emitter DC current gain is (a) 51 (b) 49 (c) 1 (d) 0.02

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Electrical comprehension tests are used to assess your competence in the use of electrical concepts.

SAMPLE QUESTION: What does the following symbol represent?

In the following circuit, what happens if the switch remains open?

In the following circuit, if switch A closes and switch B remains open, what will happen?

In the following circuit, with switch A open, which bulbs are illuminated (if any)?

If switch B remains open, what will happen? 12 V Battery

In the following electrical circuit, if switch A closes and switch B and switch C remain open, what will happen?

In the following circuit, how many bulbs will illuminate if switch 3 closes?

In the following circuit, how many bulbs will illuminate if switches 1 and 5 close?

Which of the following symbols represents a speaker? TIMER

Which of the following symbols represents a heating element?

Which of the following symbols represents a variable TIMER

ELECTRONIC CIRCUIT SYMBOLS

Which type of electrical device only allows current in one direction?

What is covered on wires to guard the

Try another one...

What does the DC stand for in the term 'DC electricity'?

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Intro

Which of the following code is also known as reflected code A. Excess 3 codes B. Grey code C. Straight binary code D. Error code

In to encode a negative number first the binary representation of its magnitude is taken complement each bit and then add 1 A Signed integer representation

The output of an OR gate is LOW when A. all inputs are LOW B. any input is LOW

Convert the fractional binary number 0000.1010 to decimal. A 0.625 B 0.50

How is a J-K flip-flop made to toggle? A. J = 0, K = 0

IC chip used in digital clock is A.SSI

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CLOSED CIRCUIT

RESISTOR

CAPACITOR

TRANSISTOR

SWITCH

5à5Ö5ä5 5biy5êl**5ãtrö5m5agtryo592558/5ó5õtsvsy5iy5a5sö5ağb |25£5îftyute5**í|5**id5ix5o5ð5 15yve**r5ú5î5ô5û5ü - \$à5Ö5 Electronics, | Part-1 | electro magno POWER ELECTRONICS MCQ's, ...

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Knowledge

the complete cycle of the input signal half cycle of the input signal less than half cycle of the input signal one-fourth cycle of the input signal

Half wave rectifier Full wave rectifier Bridge rectifier Three phase full wave rectifier

electrical connection to external Ckt. physical strength isolation

Vacuum triode FET SCR Both (a) and (b)

Phosphorus Boron Arsenic Antimony

due to rapture of covalent band mostly in germanium junctions in lightly doped junctions due to thermally generated minority carriers

total surface area resistance value

Active Saturation Cut off Reverse active

formation of P-type semiconductor more free electrons than holes in the semiconductor antimony concentrating on the edges of the crystals increased resistance

high purity silicon high purity silica heavily doped polycrystalline silicon epitaxial grown silicon C. emitter is positive with respect to base and base is positive with respect to collector D. emitter is negative with respect to base and base is positive with respect to collector

reverse biased forward biased biased to breakdown

emitter base junction collector base junction collector either (a) or (b)

transition zone depletion region neutral region active region

resistance of diode conductance of diode incremental resistance of diode incremental conductance of diode

reverse bias exceeds the limiting value forward bias exceeds the limiting value forward current exceeds the limiting value potential barrier is reduced to zero

a hole is created a proton is also lost atom becomes an ion rest of the electron move at a faster rate forward biased reverse biased either forward or reverse biased

infrared region ultraviolet region

Cut off Saturation Forward active Reverse active

rolling casting casting electrolytic refining induction heating

gate voltage drain current source current all of the above

phase shift oscillator weinbridge oscillator colpitt's oscillator clapp oscillator

impure semi-conductor dipole semi-conductor bipolar semi-conductor extrinsic semi-conductor applied electric field concentration gradient of charge carrier square of the electric field cube of the applied electric field

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Intro

THE GATE OXIDE THICKNESS IN THE MOS CAPACITOR IS

THE MAXIMUM DEPLETION LAYER WIDTH IN SILICON IS

THE RATIO OF MOBILITY TO DIFFUSION COEFFICIENT IN A SEMICONDUCTOR HAS THE UNITS

IN AN N TYPE SILICON CRYSTAL AT ROOM TEMPERATURE, WHICH OF THE FOLLOWING CAN PEAK ELECTRIC FIELD IN PN JUNCTION DEVICE AT ROOM TEMPERATURE

AT ROOM TEMPERATURE, THE POSSIBLE VALUE FOR THE MOBILITY OF ELECTRONS IN THE INVERTION LAYER OF A SILICON N CHANNEL MOSFET IS

DRIFT CURRENT IN SEMICONDUCTOR DEPENDS UPON

AP+N JUNCTION HAS BUILT IN POTENTIAL OF 0.8V. THE DEPLETION LAYER WIDTH AT A REVERSE BIAS OF 1.2 V OF 2um. FOR A REVERSE BIAS OF 7.2 V. THE DEPLETION LAYER WIDTH WILL BE

IN A P-N JUNCTION DIODE UNDER REVERSE BIAS. THE MAGNITUDE OF ELECTRIC FIELD IS MAXIMUM AT

CONSTANT CURRENT AT ROOM TEMPERATURE. WHEN THE TEMPERATURE IS INCREASED BY 10°C, THE FORWARD BIAS VOLTAGE ACROSS TH PN JUNCTION

IN A FORWARD BIASED PN JUNCTION DIODE. THE SEQUENCE OF EVENTS THAT BEST DESCRIBES THE MECHANISM OF CURRENT FLOW IS

FOR MOSFET WHEN CHANNEL LENGTH REDUCES VTH ALSO REDUCES AND

CONVERSION EFFICIENCY OF SILICON SOLAR CELL IS

A SEMICONDUCTOR PHOTO DIODE USES

IN AN INTRINSIC SEMICONDUCTOR THE FREE ELECTRON CONCENTRATION DEPENDS ON THE DIFFUSION POTENTIAL ACROSS A PN JUNCTION

A ZENER DIODE WORKS ON THE PRINCIPLE OF

IN A TUNNEL DIODE, IMPURITY CONCENTRATION IS OF THE ORDER OF

IN A TUNNEL DIODE, DEPLETION LAYER OF WIDTH IS OF ORDER

TUNNEL DIODE IS A PN DIODE WITH

FOR GERMANIUM AT ROOM TEMPERATURE. CRITICAL WAVELENGTH FOR PHOTO CONDUCTION IS

RESPONSE TIME OF PIN DIODE IS OF THE ORDER OF

THE TRANSITION REGION IN AN OPEN CIRCUITED PN JUNCTION CONTAINS

IN A GE DIODE, REVERSE SATURATION CURRENT IS OF THE ORDER OF

CRITICAL VOLTAGE OF BARITT DIODE DEPENDS ON

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